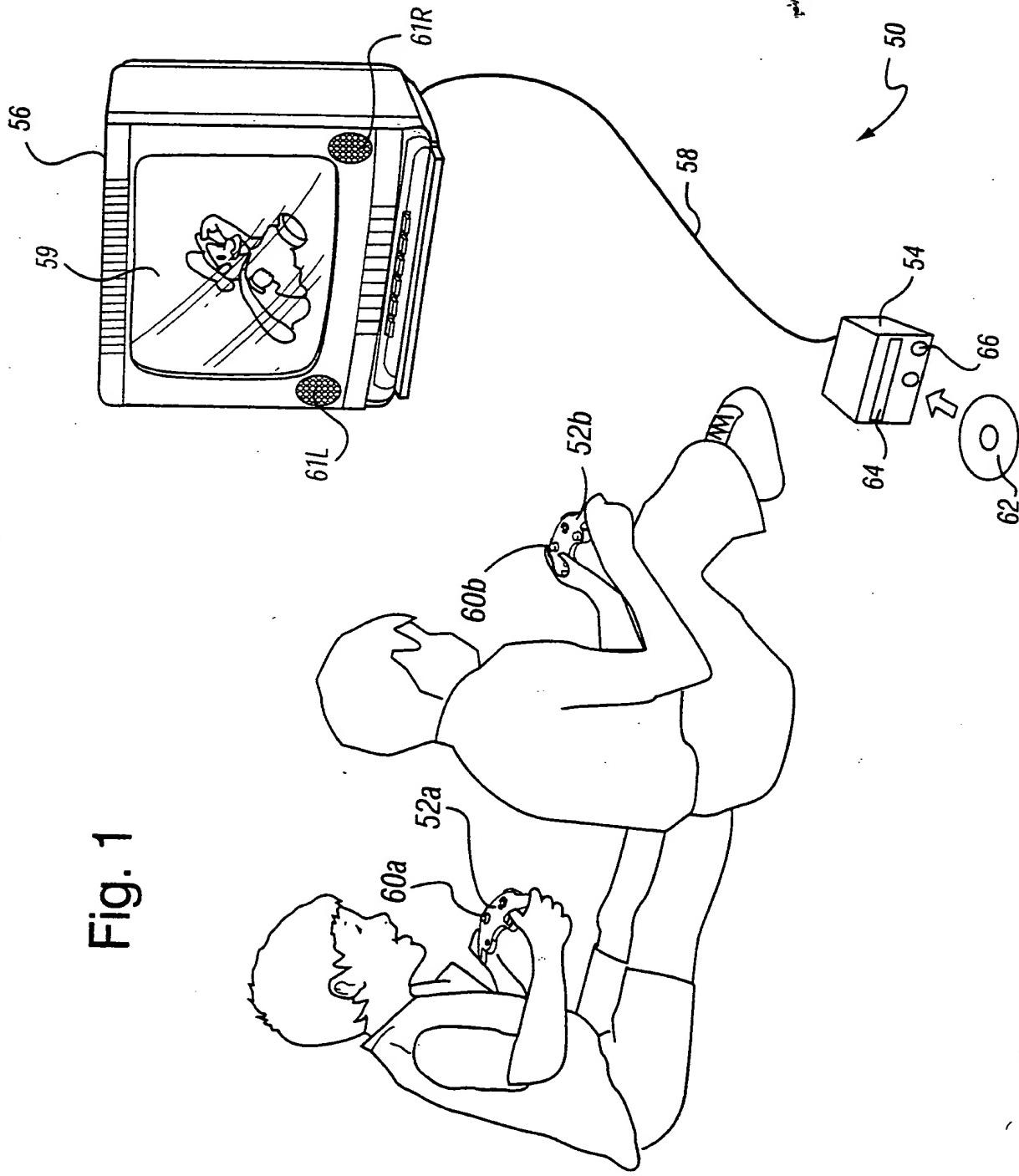


Fig. 1



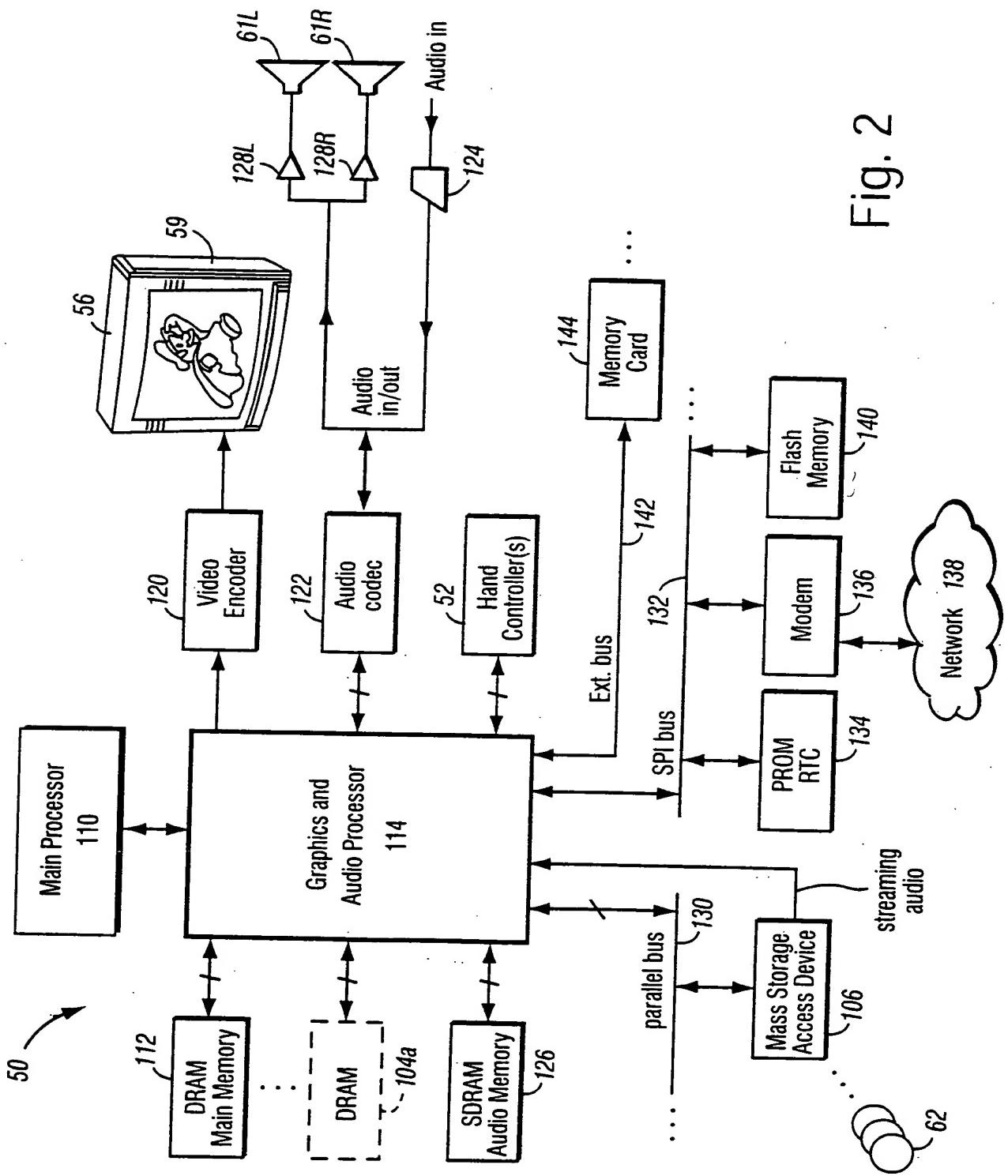
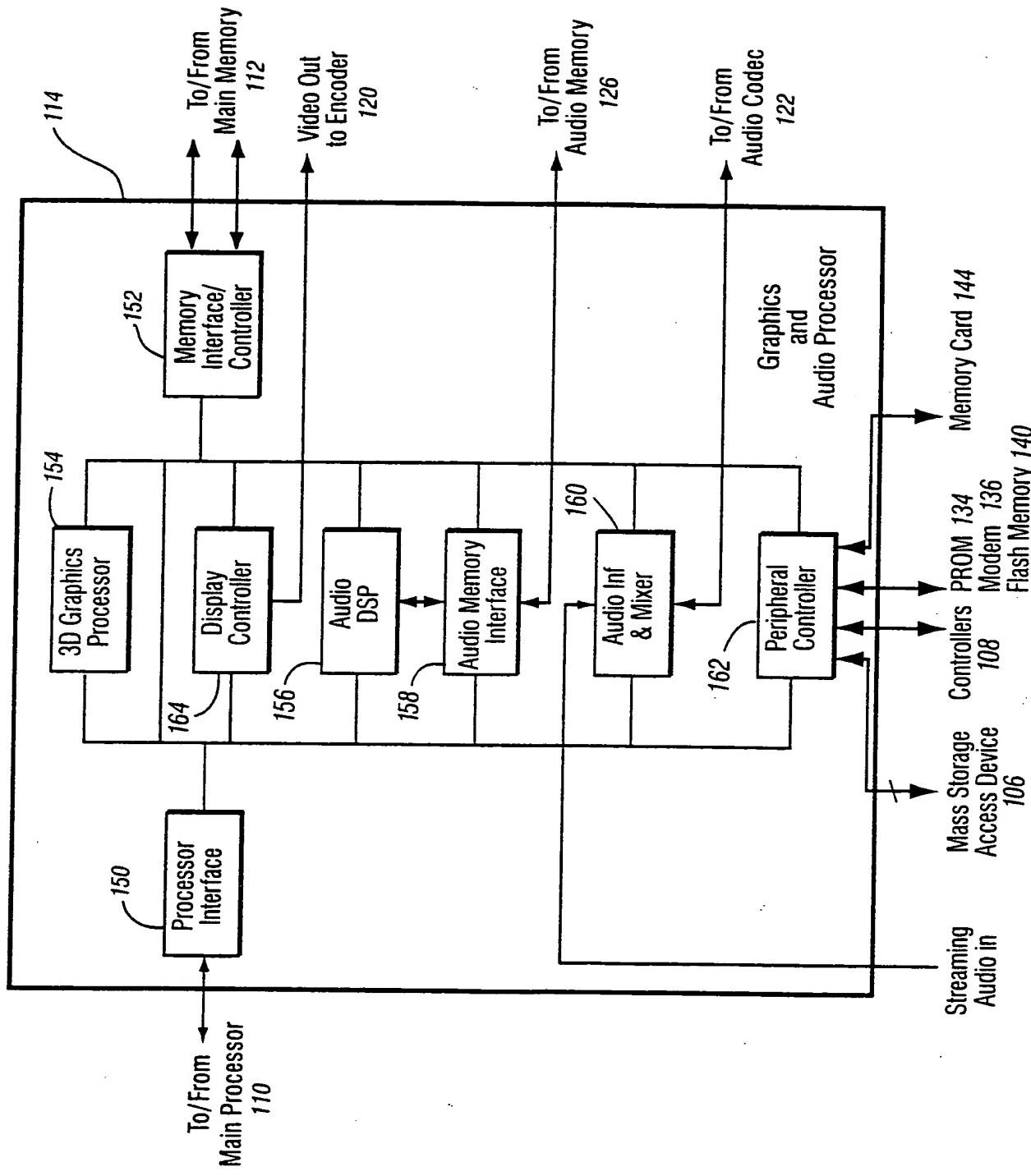


Fig. 2

Fig. 3



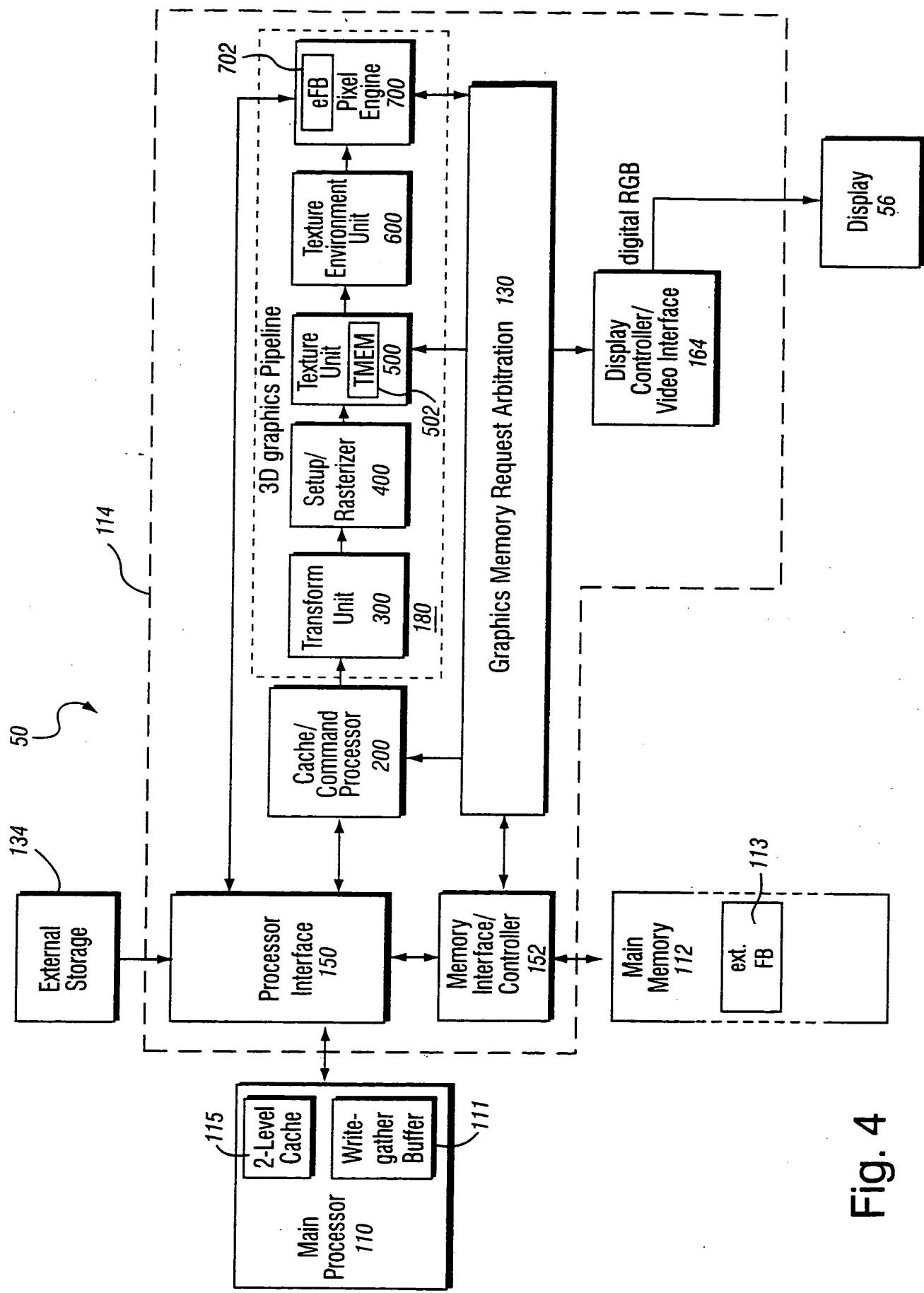


Fig. 4

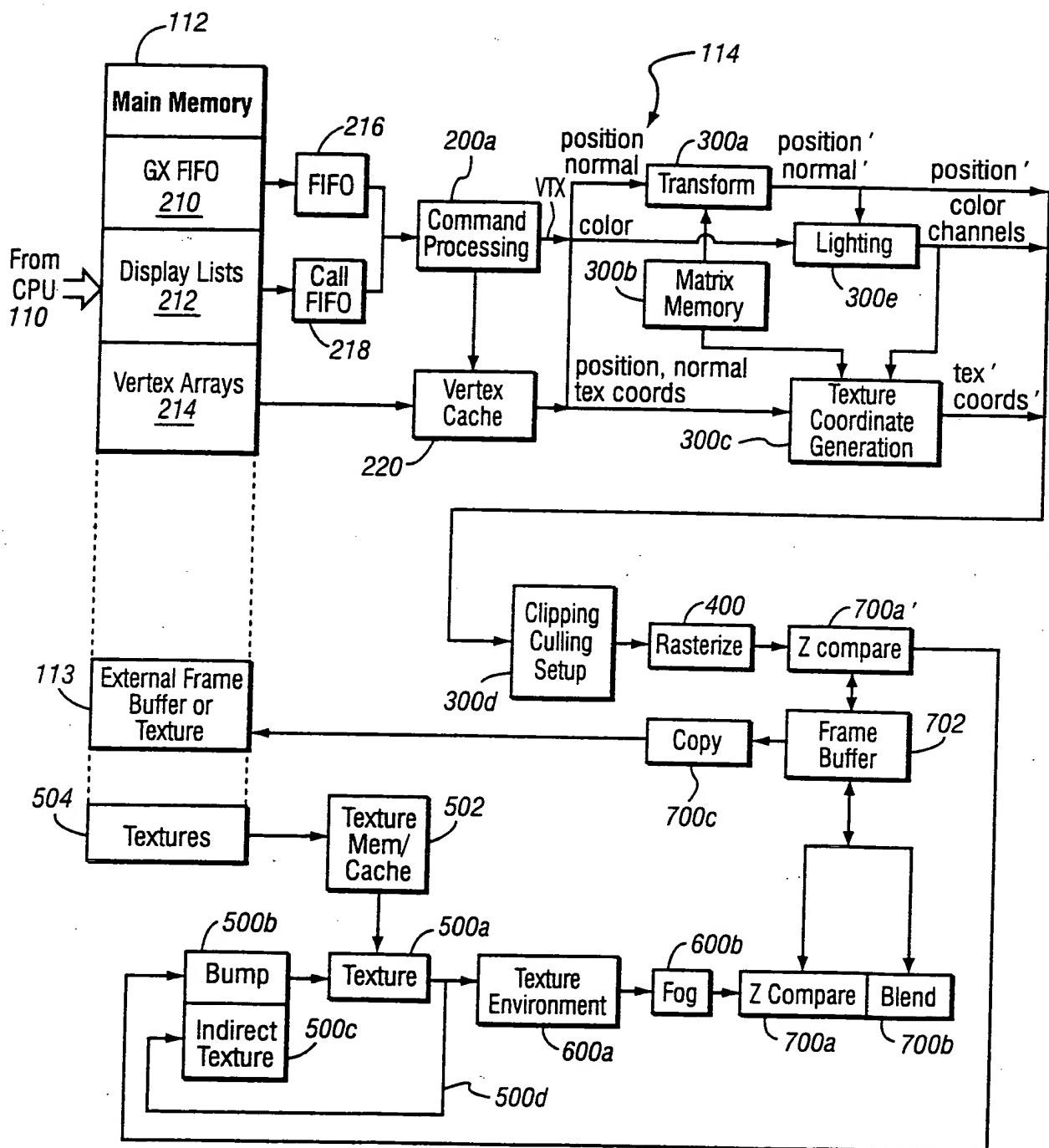


Fig. 5 EXAMPLE GRAPHICS PROCESSOR FLOW

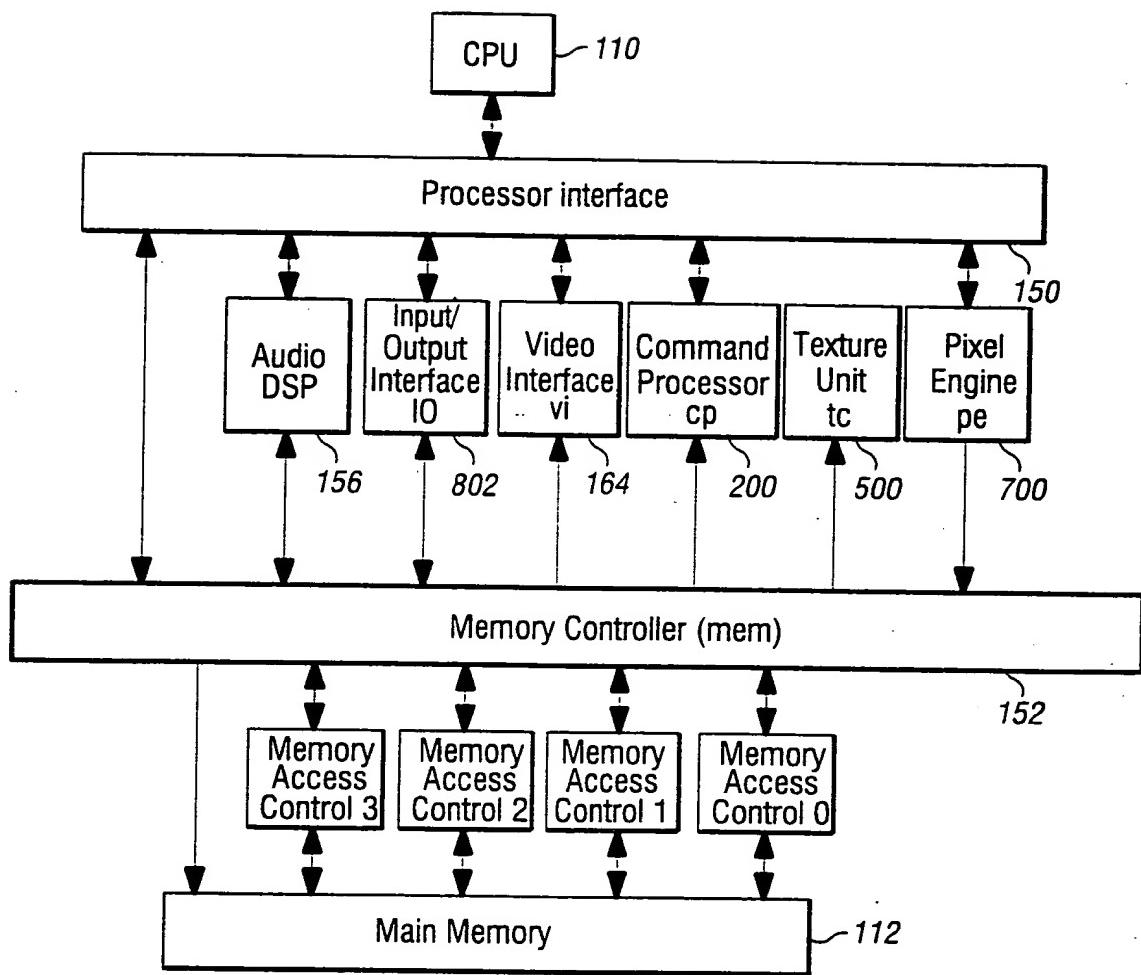


Fig. 6A

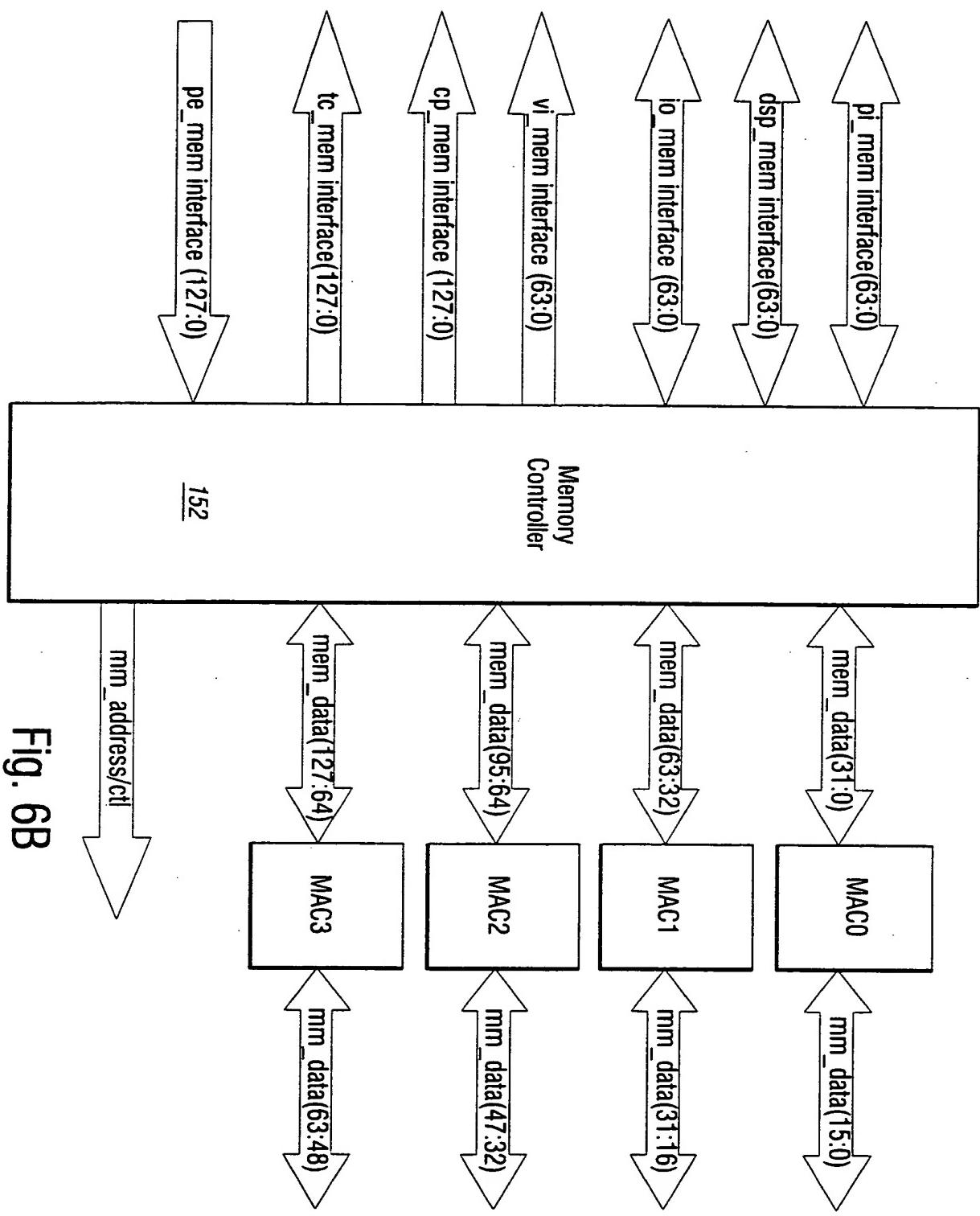


Fig. 6B

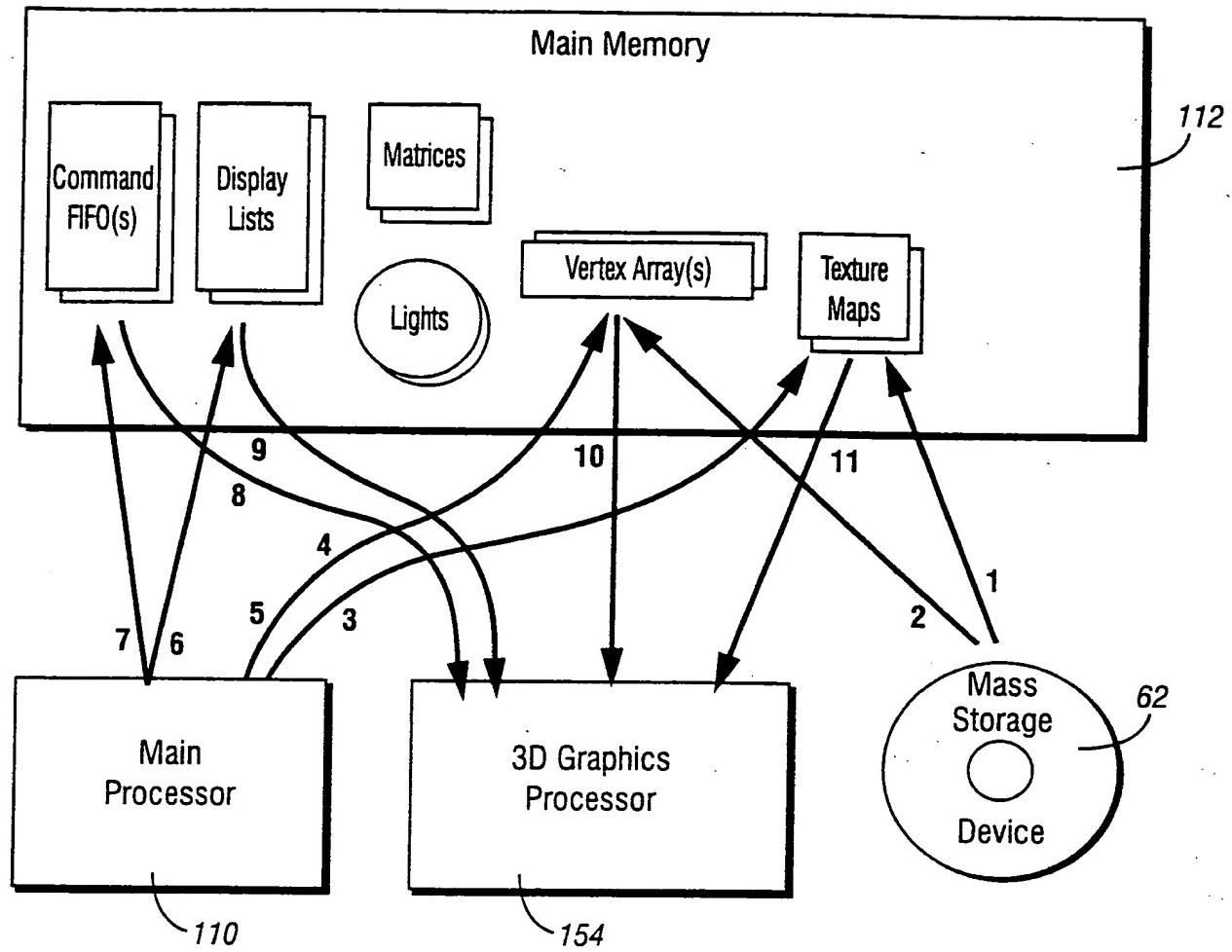


Fig. 7

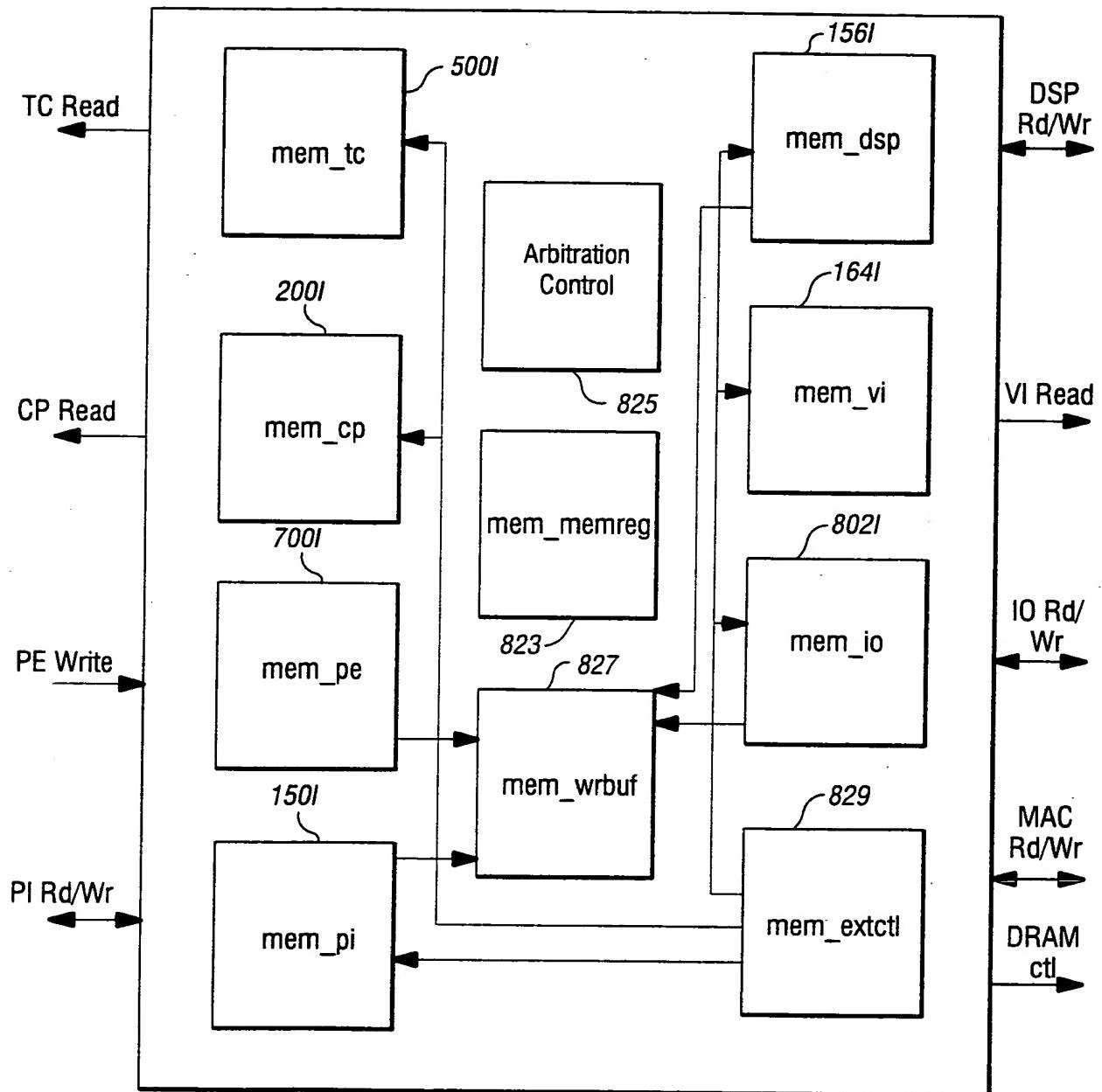


Fig. 8

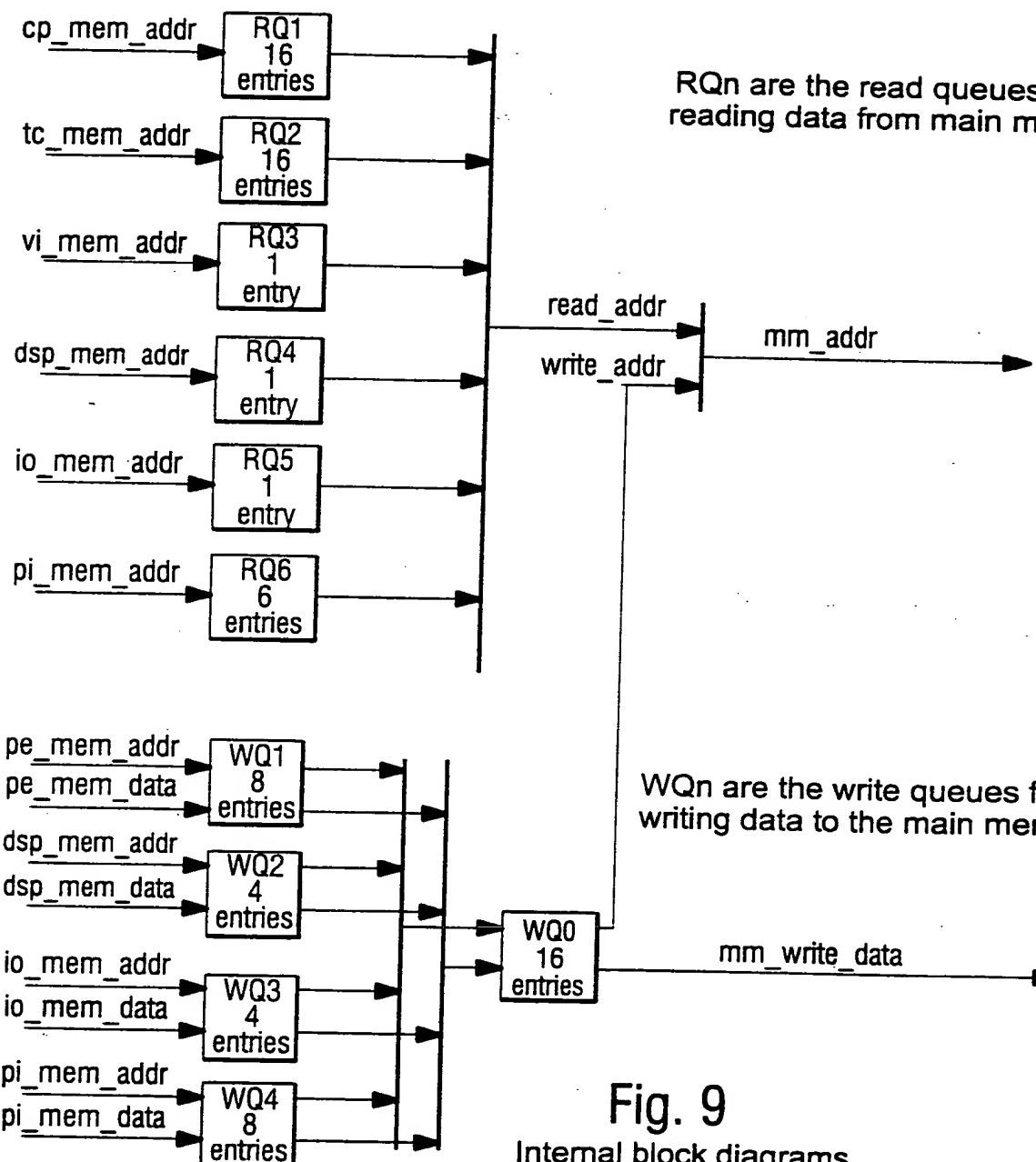


Fig. 9
Internal block diagrams

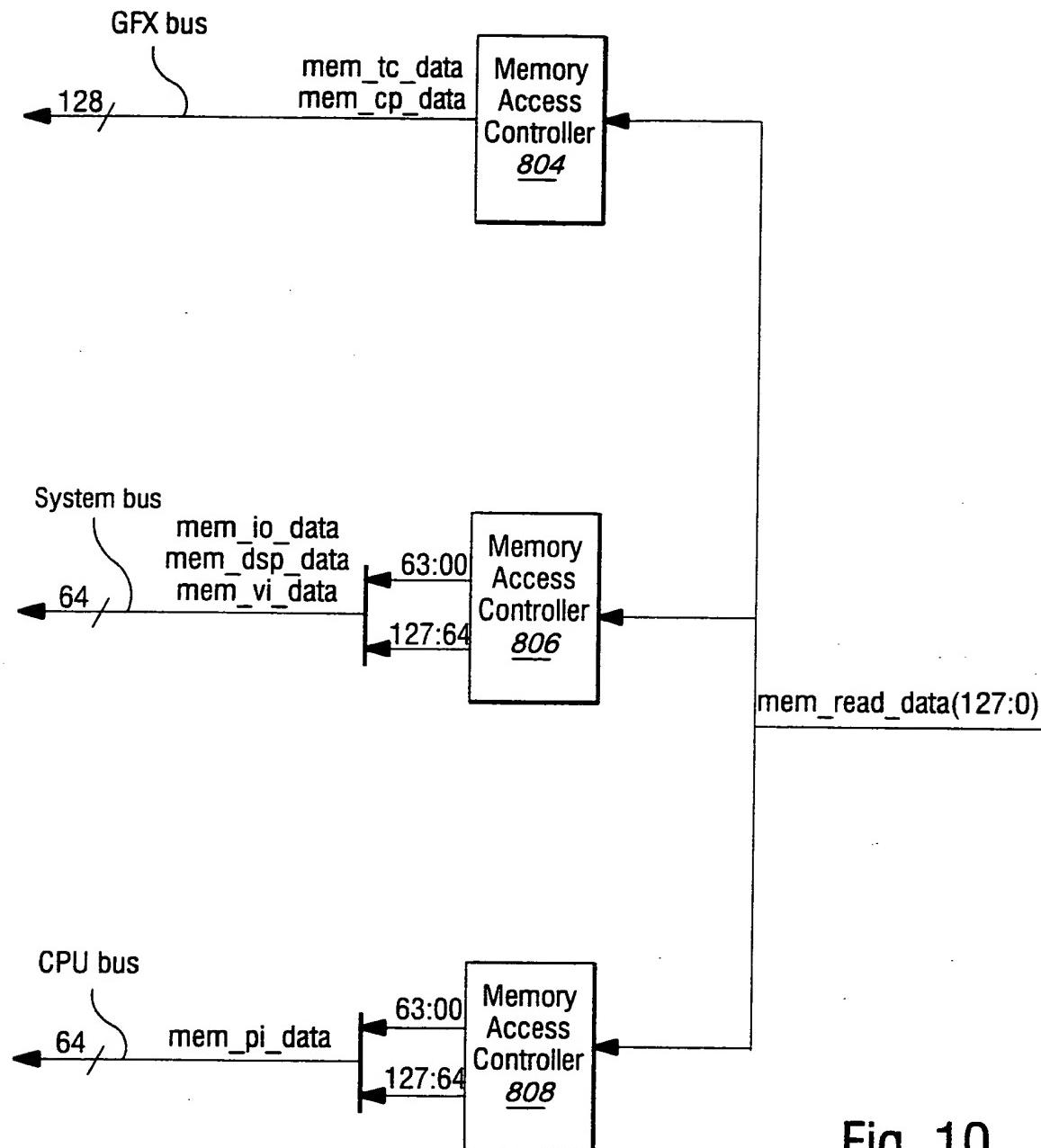


Fig. 10
Read Data Path

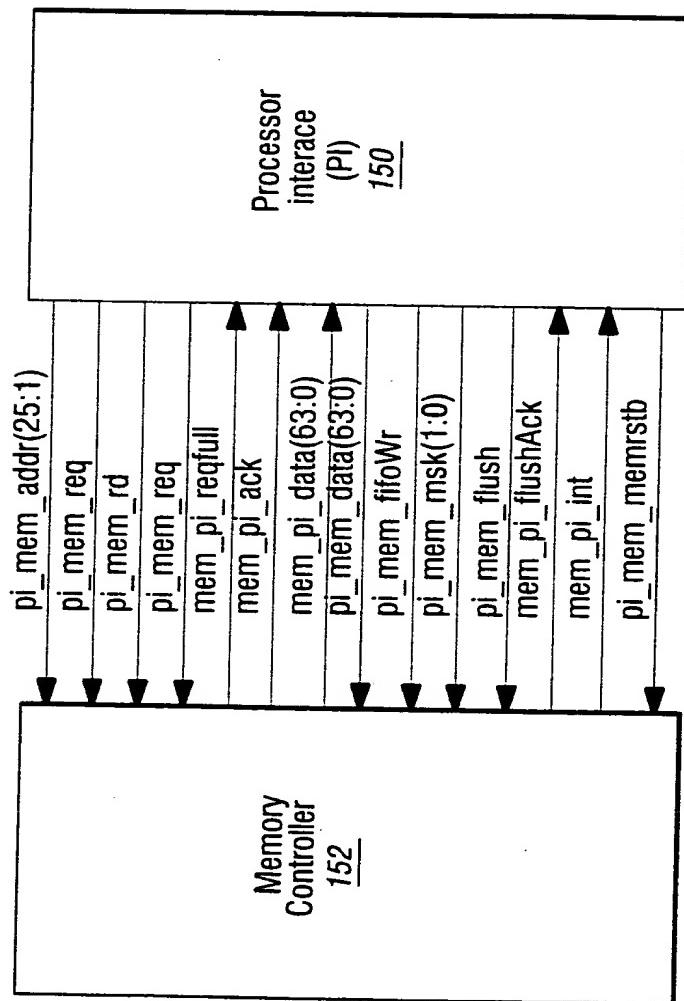


Fig. 11

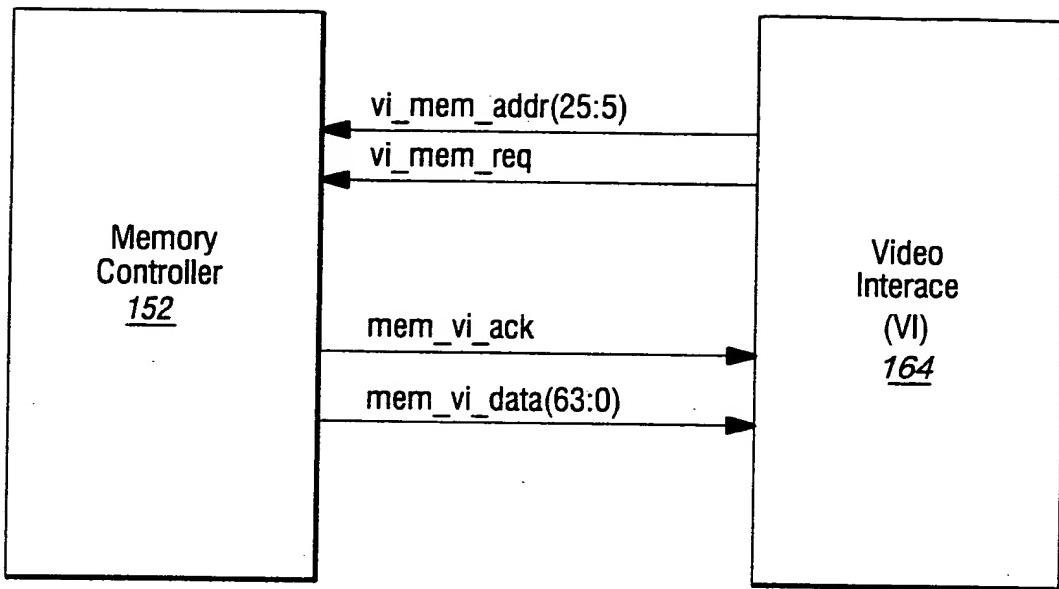


Fig. 12

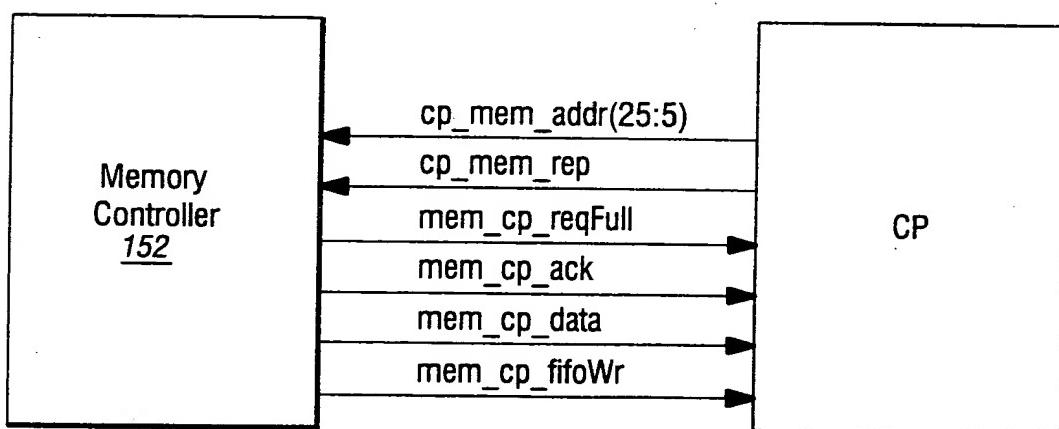


Fig. 13

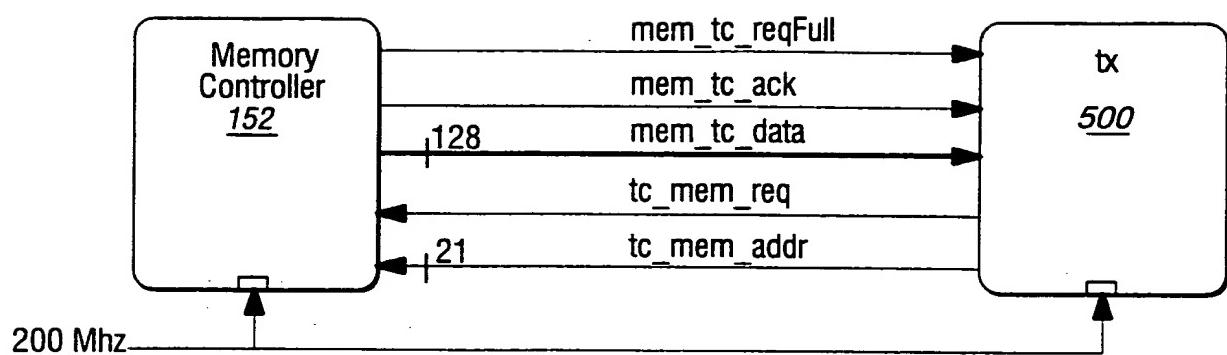


Fig. 14

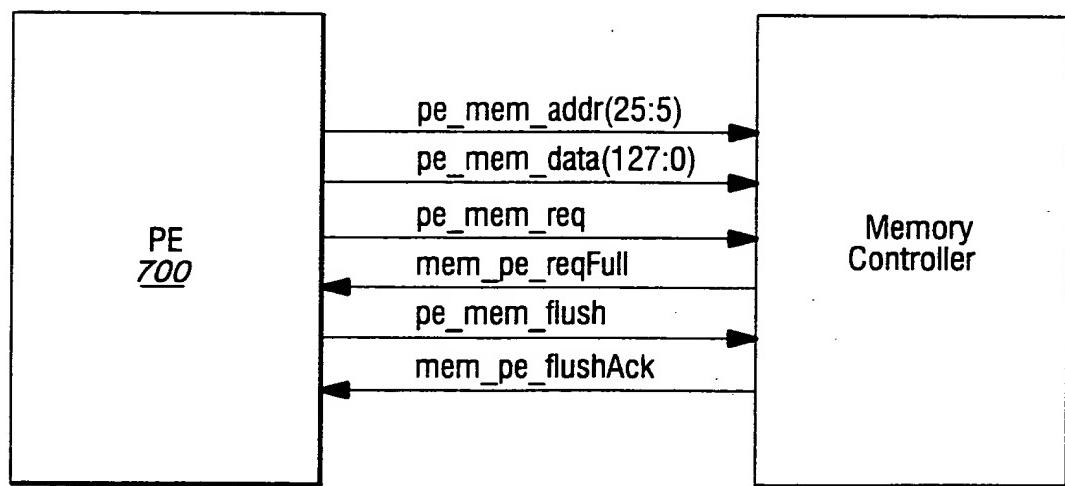


Fig. 15

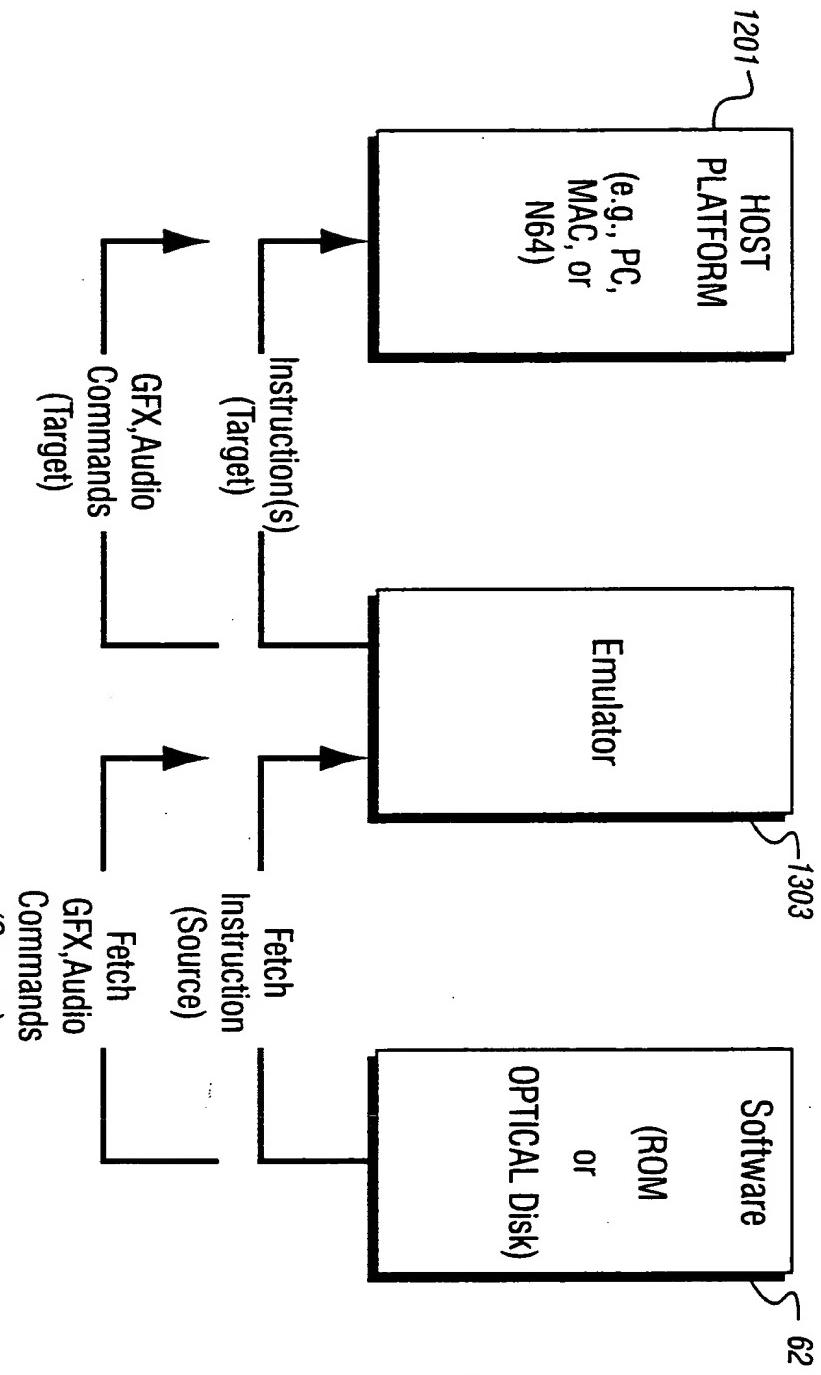


Fig. 16A

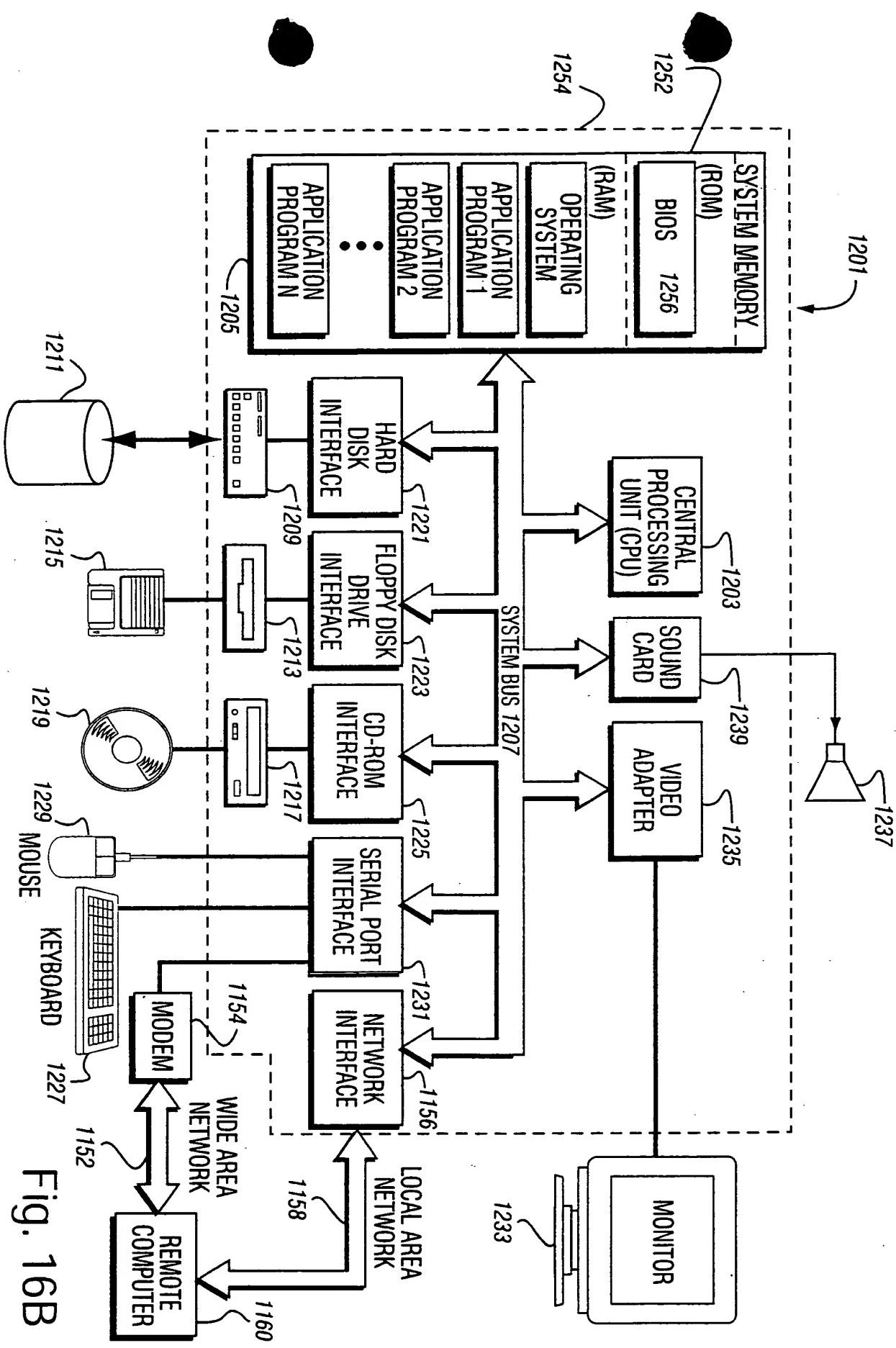


Fig. 16B